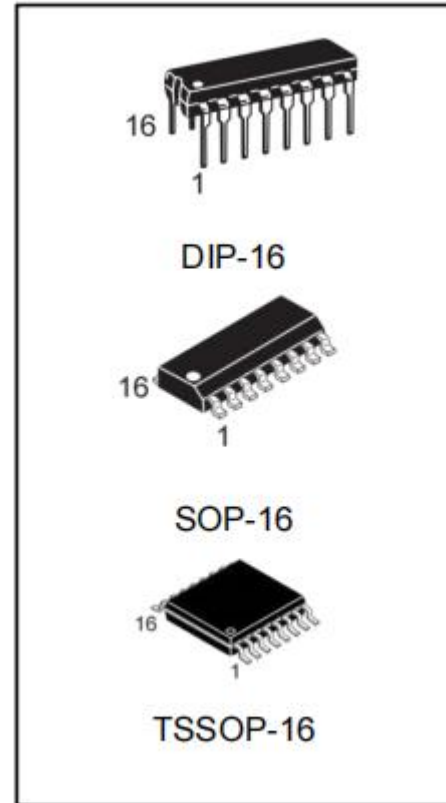


General Description

The CD4528B is a dual mono stable multi vibrator. Each device is re trigger able and reset table. Trggering can occur from either the rising or falling edge of an input pulse, resulting in an output pulse over a wide range of widths. Pulse duration and accuracy are determined by external timing components Rx and Cx.

Features

- Wide supply voltage range: 3.0V to 18V
- Separate reset available
- Quiescent current = 5.0 nA/package (typ.) at 5.0 VDC
- Diode protection on all inputs
- Trigger able from leading or trailing edge pulse
- Capable of driving two low-power TTL loads or one low power Schottky TTL load over the rated temperature range

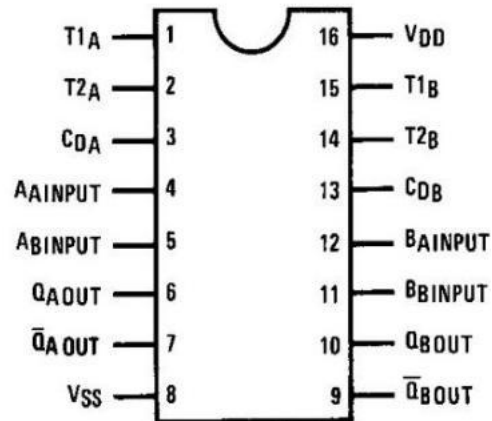


Order Information

DEVICE	Package Type	MARKING	Packing	Packing Qty
CD4528BE/ CD4528BN	DIP-16	CD4528B	TUBE	1000pcs/box
CD4528BM/TR	SOP-16	CD4528B	REEL	2500pcs/reel
CD4528BMT/TR	TSSOP-16	CD4528B	REEL	2500pcs/reel



Connection Diagram



DIP-16/SOP-16/TSSOP-16

Truth Table

Inputs			Outputs	
Clear	A	B	Q	\bar{Q}
L	X	X	L	H
X	H	X	L	H
X	X	L	L	H
H	L	↓	⌊	⌋
H	↑	H	⌋	⌊

H = HIGH Level

L = LOW Level

↑ = Transition from LOW-to-HIGH

↓ = Transition from HIGH-to-LOW

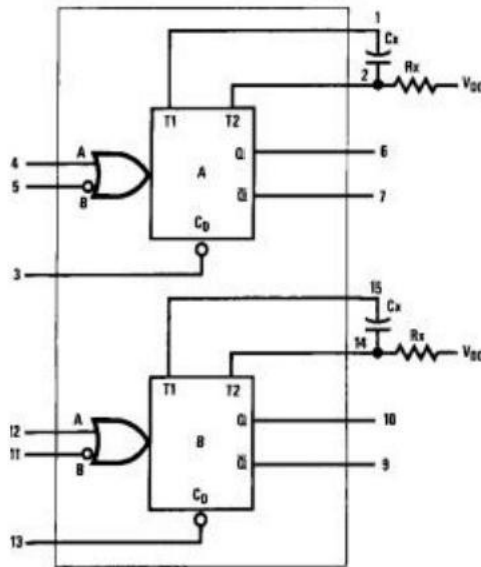
⌋ = One HIGH Level Pulse

⌊ = One LOW Level Pulse

X = Irrelevant

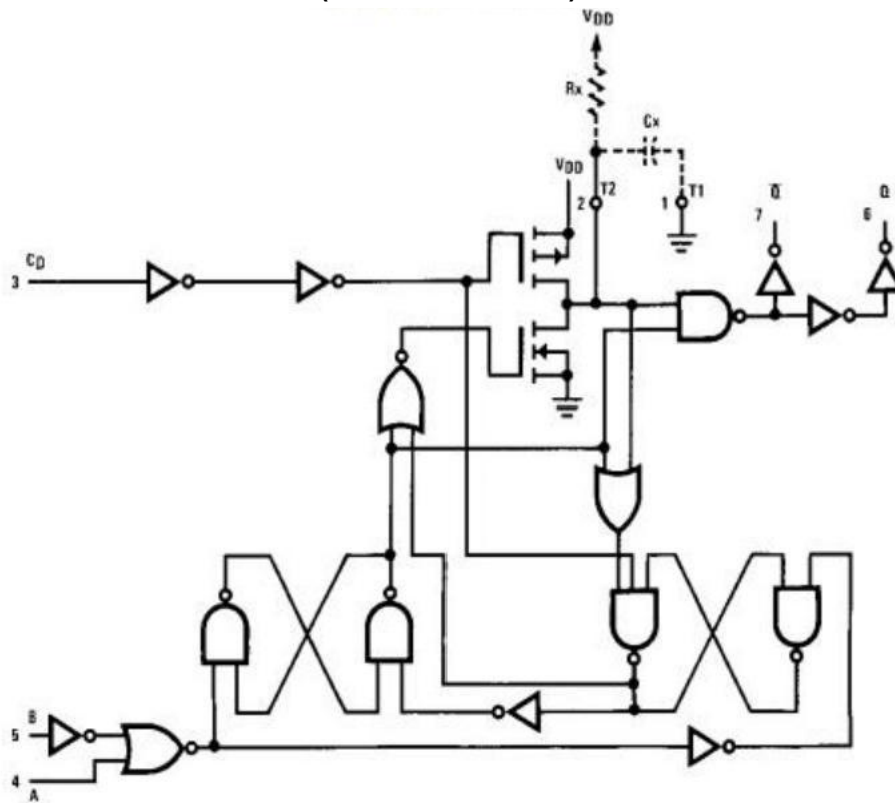


Block Diagram



Logic Diagram

(1/2 of Device Shown)



Note: Externally ground pins 1 and 15 to pin 8.

Absolute Maximum Ratings

Condition		Min	Max	UNITS
DC Supply Voltage(V _{oo})		-0.5	+18	V _{oc}
Input Voltage,All Inputs(V _{in})		-0.5	+0.5	V _{oc}
Storage Temperature Range(T _s)		-65	+150	°C
Power Dissipation(P _p)	Dual-In-Line		700	mW
	Small Outline		500	mW
Lead Temperature(T _L)(Soldering,10 seconds)			245	°C

Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is intended to be functional, but specific performance is not ensured.

Recommended Operating Conditions

Condition		Min	Max	UNITS
DC Supply Voltage(V _{op})		3	15	V
Input Voltage,(V _{in})		0 to V _{oo} V _{bc}		
Operating Temperature Range(T _A)		-40	+85	°C

DC Electrical Characteristics (Note 1)

Symbol	Parameter	Conditions	40°C		25°C			85°C		Units
			Min	Max	Min	Typ	Max	Min	Max	
I _{po}	Quiescent Device Current	V _{op} =5V		20		0.005	20		150	μA
		V _{op} =10V		40		0.010	40		300	μA
		V _{pp} =15V		80		0.015	80		600	μA
V _{or}	LOW Level Output Voltage	V _{op} =5V		0.05			0.05		0.05	V
		V _{pp} =10V		0.05			0.05		0.05	V
		V _{oo} =15V		0.05			0.05		0.05	V
V _{oH}	HIGH Level Output Voltage	V _{oo} =5V	4.95		4.95	5.0		4.95		V
		V _{op} =10V	9.95		9.95	10.0		9.95		V
		V _{op} =15V	14.95		14.95	15.0		14.95		V
V _L	LOW Level Input Voltage	V _{oo} =5V, V _o =0.5V or 4.5V		1.5		2.25	1.5		1.5	V
		V _{oo} =10V, V _o =1V or 9V		3.0		4.50	3.0		3.0	V
		V _{oo} =15V, V _o =1.5V or 13.5V		4.0		6.75	4.0		4.0	V
V _H	HIGH Level Input Voltage	V _{oo} =5V, V _o =0.5V or 4.5V	3.5		3.5	2.75		3.5		V
		V _{oo} =10V, V _o =1V or 9V	7.0		7.0	5.50		7.0		V
		V _{oo} =15V, V _o =1.5V or 13.5V	11.0		11.0	8.25		11.0		V
I _{ol}	LOW Level Output Current (Note 2)	V _{od} =5V, V _o =0.4V	0.52		0.44	0.88		0.36		mA
		V _{op} =10V, V _o =0.5V	1.3		1.1	2.25		0.9		mA
		V _{bp} =15V, V _o =1.5V	3.6		3.0	8.8		2.4		mA
I _{oH}	HIGH Level Output Current (Note 2)	V _{od} =5V, V _o =4.6V	-0.2		-0.16	-0.36		-0.12		mA
		V _{od} =10V, V _o =9.5V	-0.5		-0.4	-0.9		-0.3		mA
		V _{oo} =15V, V _o =13.5V	-1.4		-1.2	-3.5		-1.0		mA
I _{in}	Input Current	V _{oo} =15V, V _{in} =0V		-0.3		-10-5	-0.3		-1.0	μA
		V _{bp} =15V, V _{in} =15V		0.3		10-5	0.3		1.0	μA

Note 1: V_{ss} = 0V unless otherwise specified.

Note 2: I_{oH} and I_{oL} are tested one output at a time.

AC Electrical Characteristics (Note 3)

TA=25° C, CL=50 pF, RL=200 kQ, Input tr=tf=20 ns, unless otherwise specified

Symbol	Parameter	Conditions	Min	Typ	Max	Units
tr	Output Rise Time	tr=(3.0 ns/pF)CL+30 ns, Voo =5.0V		180	400	ns
		tr=(1.5ns/pF)CL+15ns, Vbo=10.0V		90	200	ns
		tr=(1.1 ns/pF)Cl+10 ns, Vop =15.0V		65	160	ns
tf	Output Fall Time	tf=(1.5 ns/pF)CL+25 ns, Voo=5.0V		100	200	ns
		tf=(0.75 ns/pF)CL+12.5 ns, Vop =10V		50	100	ns
		tf=(0.55 ns/pF)CL+9.5ns, Voo=15.0V		35	80	ns
tpLH tPHL	Turn-Off, Turn-On Delay A or B to Q or Q Cx=15pF, Rx=5.0 kQ	tpLh, trHL =(1.7 ns/pF)Cl+240 ns, Voo=5.0V		230	500	ns
		tpLH, tpHL=(0.66 ns/pF)CL+8 ns, Vop=10.0V		100	250	ns
	Turn-Off, Turn-On Delay A or B to Q or G Cx=100 pF, Rx=10 kQ	tpLh, tpHL=(0.5 ns/pF)Cl+65 ns, Voo=15.0V		65	150	ns
		tpLH, tpHL=(1.7 ns/pF)CL+620 ns, Voo=5.0V		230	500	ns
tpLH tPHL	Turn-Off, Turn-On Delay A or B to Q or G Cx=100 pF, Rx=10 kQ	tpLH, tpHL=(0.66 ns/pF)CL+257		100	250	ns
		ns, Vop=10.0V tpLH, tpHL=(0.5 ns/pF)Cl+185		65	150	ns
twu twh	Minimum Input Pulse Width A or B Cx=15 pF, Rx=5.0 kQ	VDo =5V		60	150	ns
		Vbo =10.0V		20	50	ns
	Cx=1000 pF, Rx=10 kQ	Vpp =15V		20	50	ns
		Voo =5V		60	150	ns
		Vpp =10.0V		20	50	ns
		Voo =15V		20	50	ns
PWour	Output Pulse Width Q or a For Cx<0.01 μF (See Graph for Appropriate VDD Level) Cx=15 pF, Rx=5.0 kQ	Vpp =5V		550		ns
		Vpp =10.0V		350		ns
		Vpp =15V		300		ns
	For Cx>0.01 μF Use PWout =0.2 Rx Cx In [Vpo-Vss] Cx=10,000 pF, Rx=10 kQ	VDo =5V	15	29	45	μs
		Vpo =10.0V	10	37	90	μs
		Vop =15V	15	42	95	μs
t-LH tPHL	Reset Propagation Delay, tPLH, tPHL Cx=15pF, Rx=5.0 kQ	VDo =5V		325	600	ns
		Vpp =10.0V		90	225	ns
	Cx=1000 pF, Rx=10 kQ	Voo =15V		60	170	ns
		Vpp=5V		7.0		μs
		Vpo =10.0V		6.7		μs
		VpD =15V		6.7		μs
txR	Minimum Retrigger Time Cx=15pF, Rx=5.0 kQ	VDo =5V		0		ns
		Vbo =10.0V		0		nS
		Vpo =15V		0		ns
	Cx=1000 pF, Rx=10 kQ	Voo =5V		0		ns
		Vop =10.0V		0		ns
		Vpo =15V		0		ns
Pulse Width Match between Circuits in the Same Package Cx =10,000 pF, Rx =10 kQ		Voo =5V		6	25	%
		Vpo =10.0V		8	35	%
		Vpp =15V		8	35	%

Note 3: AC parameters are guaranteed by DC correlated testing.

Pulse Widths

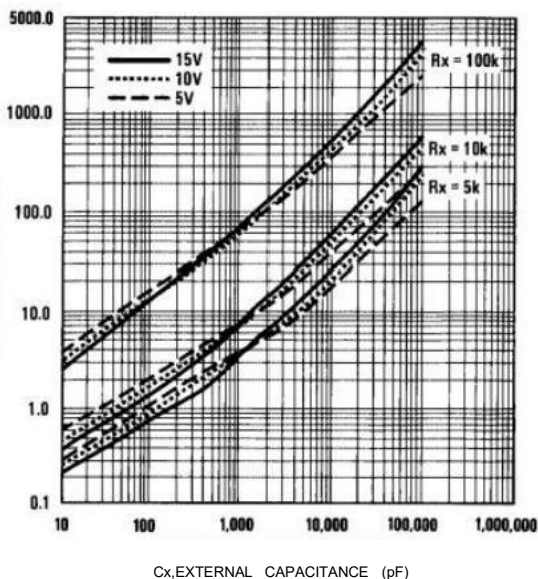


FIGURE 1. Pulse Width vs C_x

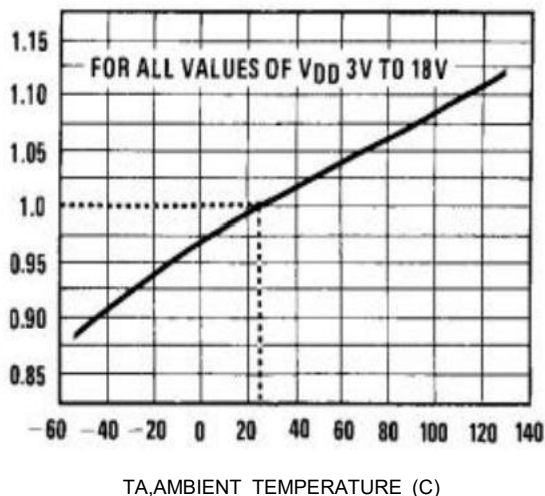
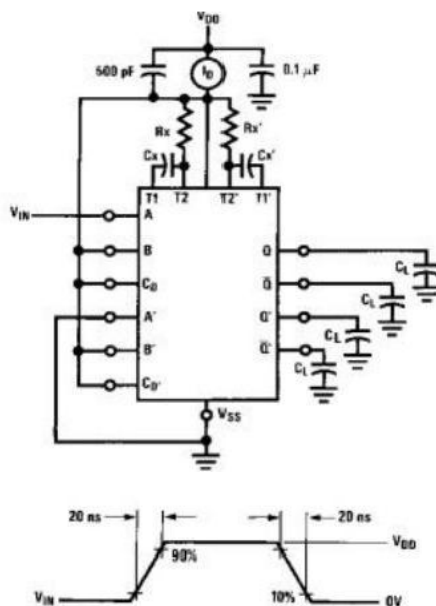


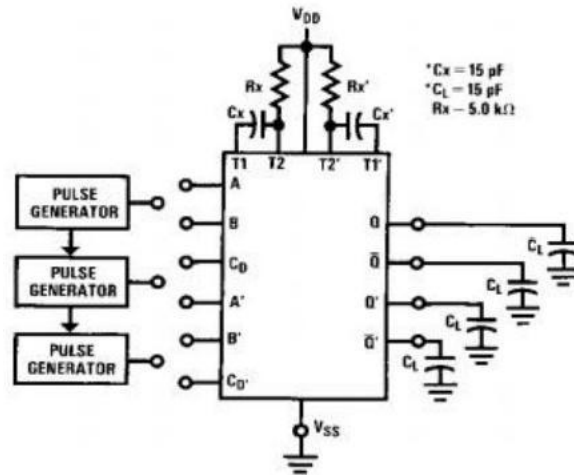
FIGURE 2. Normalized Pulse Width vs Temperature

AC Test Circuits and Wave forms



Duty Cycle = 50%

FIGURE 3. Power Dissipation Test Circuit and Waveforms



*Includes capacitance of probes, wiring, and fixture parasitic.

Note: AC test waveforms for PG1, PG2, and PG3 in Figure 4.

Input Connections

Characteristics	CD	A	B
$t_{pLH}, t_{pHL}, t_r, t_f,$ PW_{out}, PW_{in}	VDD	PG1	VDD
$t_{pH}, t_{PH}, t_r, t_f,$ PW_{out}, PW_{in}	VDD	Vss	PG2
$t_{puH(R)}, t_{pHL(R)}, PW_{in}$	PG3	PG1	PG2

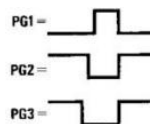


FIGURE 4.AC Test Circuit

AC Test Circuits and Wave forms (continued)

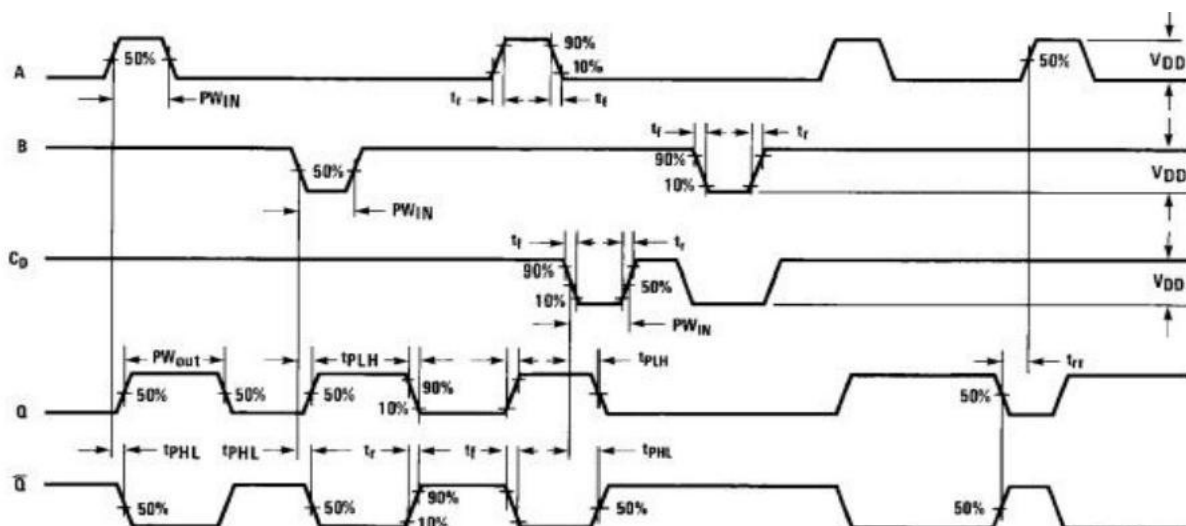
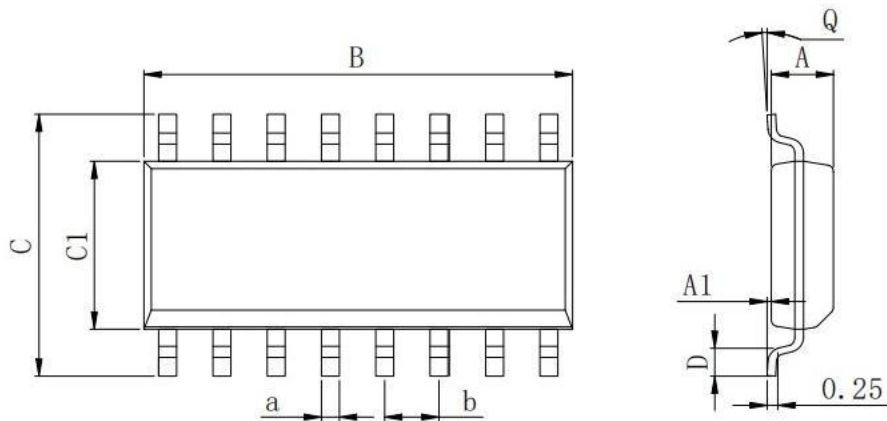


FIGURE 5.AC Test Waveforms

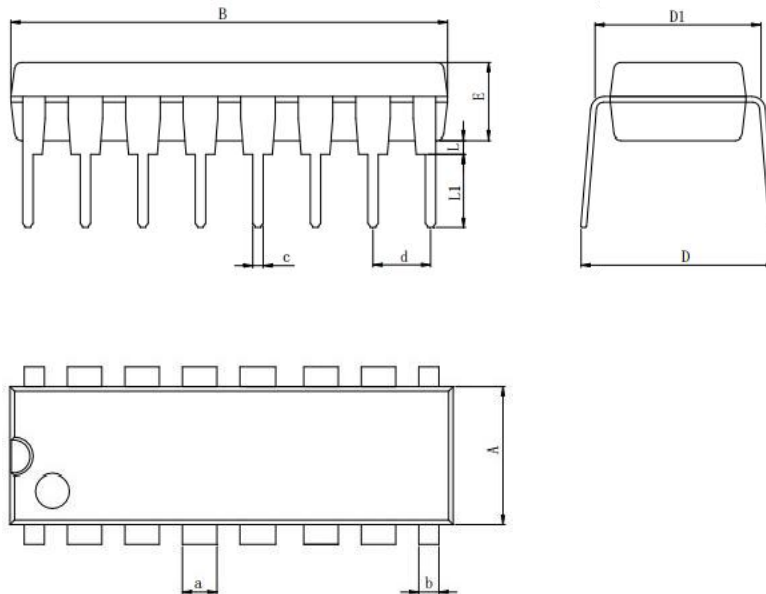
Physical Dimensions

SOP-16



Dimensions In Millimeters(SOP-16)									
Symbol:	A	A1	B	C	C1	D	Q	a	b
Min:	1.35	0.05	9.80	5.80	3.80	0.40	0°	0.35	1.27 BSC
Max:	1.55	0.20	10.0	6.20	4.00	0.80	8°	0.45	

DIP-16

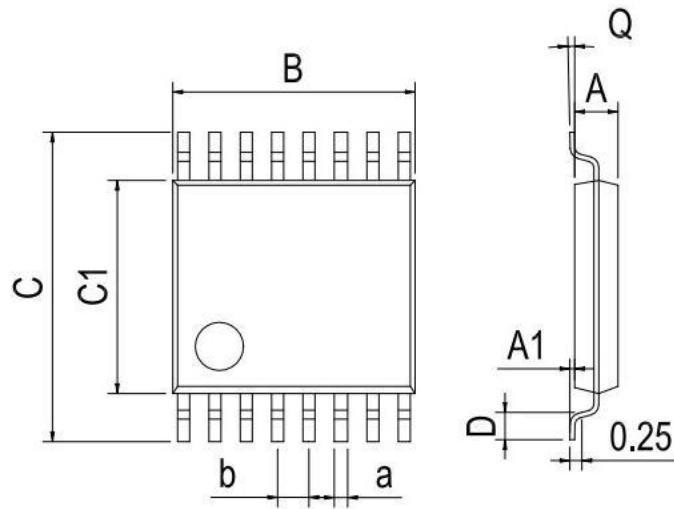


Dimensions In Millimeters(DIP-16)											
Symbol:	A	B	D	D1	E	L	L1	a	b	C	d
Min:	6.10	18.94	8.10	7.42	3.10	0.50	3.00	1.50	0.85	0.40	2.54 BSC
Max	6.68	19.56	10.9	7.82	3.55	0.70	3.60	1.55	0.90	0.50	



Physical Dimensions

TSSOP-16



Dimensions In Millimeters(TSSOP-16)									
Symbol:	A	A1	B	C	C1	D	Q	a	b
Min:	0.85	0.05	4.90	6.20	4.30	0.40	0°	0.20	0.65 BSC
Max:	0.95	0.20	5.10	6.60	4.50	0.80	8°	0.25	